

### **AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

### **Listing Of Claims:**

- 1.-18. (Canceled)
19. (New) A method for switching between at least two operating modes of a processor unit that includes at least two execution units for running programs, comprising:  
    assigning at least one identifier to at least the programs, the identifier allowing a differentiation between the at least two operating modes;  
    switching between the operating modes as a function of the identifier such that the processor unit runs the programs according to the assigned operating mode.
20. (New) The method as recited in Claim 19, wherein the programs contain task programs or constitute them, and the identifier is assigned to the corresponding individual task programs.
21. (New) The method as recited in Claim 19, wherein the programs are made up of individual program segments or contain them, and the identifier is assigned to the corresponding individual program segments.
22. (New) The method as recited in Claim 19, wherein the programs are made up of individual program instructions, and the identifier is assigned to the corresponding individual program instructions.
23. (New) The method as recited in Claim 19, wherein the programs are part of an operating system of the processor unit or constitute the operating system.
24. (New) The method as recited in Claim 19, wherein the programs are used for controlling operating sequences of a vehicle.
25. (New) The method as recited in Claim 19, wherein a first operating mode is provided which corresponds to a safety mode in which the two execution units run identical programs redundantly.

26. The method as recited in Claim 25, wherein conditions or results obtained while the programs are run are compared for agreement, errors being detected if there is a discrepancy.
27. (New) The method as recited in Claim 25, wherein the programs are run synchronously.
28. (New) The method as recited in Claim 19, wherein in the second operating mode, which corresponds to a performance mode, each execution unit runs different programs.
29. (New) The method as recited in Claim 19, wherein the identifier is in the form of at least one bit.
30. (New) The method as recited in Claim 19, wherein a program instruction provided that generates an identifier indicating if the program is to be run in the first or second operating mode.
31. (New) The method as recited in Claim 19, wherein the identifier is written to a specific memory area.
32. (New) The method as recited in Claim 31, wherein the identifier is generated by an instruction provided in an instruction set of the processor unit.
33. (New) The method as recited in Claim 32, wherein the identifier is generated by a write instruction.
34. (New) A device for switching between at least two operating modes of a processor unit that includes at least two execution units for running programs, comprising:
  - an arrangement for assigning at least one identifier to at least the programs, the identifier allowing a differentiation between the at least two operating modes;
  - an arrangement for switching between the operating modes as a function of the identifier such that the processor unit runs the programs according to the assigned operating mode.
35. (New) The device as recited in Claim 34, further comprising:
  - at least duplicate arithmetic-logic units provided correspondingly as at least two execution units.

36. (New) A processor unit for running programs, comprising:
- at least two execution units;
  - a switching arrangement via which it is possible to switch between at least two operating modes of the processor unit, wherein:
    - the switching arrangement assigns at least one identifier to at least the programs, the at least one identifier allowing a differentiation between the two operating modes, and the switching arrangement being designed in such a way that the switching arrangement switches between the operating modes as a function of the identifier, and the processor unit runs the programs according to the assigned operating mode.